

J1011 U.S. PTO
J10/020407



BEST AVAILABLE COPY

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL. NUM	FILING DATE	CLASS	SUBCLASS	GAI 2822 4842	EXAMINER <i>COTHORN Trinh</i>
10020407 12/12/2001 438 14					
**APPLICANTS: Madge Robert; Cota Kevin; Whitefield Bruce;					
**CONTINUING DATA VERIFIED:					
**FOR FOREIGN APPLICATIONS VERIFIED:					
PG-PUB. DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>		ATTORNEY DOCKET NO	
Foreign priority claimed US 2001-119 or conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no		01-299 71730	
Title: A method of detecting spatially correlated variations in a parameter of an integrated circuit die					
US DEPT. OF COMM. PAT. & TMDL-PTO-4596 (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
		Assistant Examiner	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.
TERMINAL		Print Fig.	
DISCLAIMER		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

DISK (CRF)

CD-ROM
(Attached in pocket on right inside flap)